

Abstract

A DSX jack module having removable jacks is disclosed. The jack module includes a jack mount for holding the jacks. The jack module also includes a front facing cross-connect array and a front facing IN/OUT array. A circuit board is
5 electrically connected to the arrays. The circuit board includes portions positioned directly behind the cross-connect array and the IN/OUT array. A chassis for holding jack modules is also disclosed. The chassis includes a front door having a double hinge configuration.

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